SHARDA UNIVERSITY
Beyond Boundaries

A+ NAAC

Advertisement for Ph.D. Position (full-time) under Visvesvaraya PhD Scheme for Electronics & IT (Phase-II), Ministry of Electronics and Information Technology, Govt. of India

Advt. No: SU/ECE/2026-27/	**Date: 07/09/2026**
Applications are invited from interested and highly motivated candidates for the full-time Ph.D. under 'Visvesvaraya PhD Scheme for Electronics & IT (Phase-II)', funded by Ministry of Electronics and Information Technology, Govt. of India. The other details are as follows.	
Position/admission level	Research Intern
Funding Agency Name	**Digital India Corporation,** Ministry of Electronics and Information Technology, Govt. of India under 'Visvesvaraya PhD Scheme for Electronics & IT (Phase-II)'
Number of Vacancies	One (01)
Project Title	Electronics System Design and Manufacturing (ESDM) –Quantum Sensing
Principal Investigator & Department	Prof. (Dr.) Pallavi Gupta, Department of Electrical Electronics and Communication Engineering (EECE), Sharda University, Greater Noida.
Place of Work	Department of Electrical Electronics and Communication Engineering (EECE), Sharda University, Knowledge Park-III, 201310, Greater Noida, U.P., India
Duration and amount of fellowship	05 years or till PhD completion (whichever is earlier) Fellowship: Rs. 46250.00+24% HRA per months (for 1st & 2nd years of PhD) Rs. 52500.00+24% HRA per month (for 3rd, 4th, & 5th years of PhD)
Job Description	1. Conduct research and experimental/simulation studies in the assigned area of Electronics & Information Technology under the guidance of the supervisor. 2. Develop innovative solutions/prototypes and analyze research results for achieving project objectives. 3. Publish research findings in reputed journals/conferences and contribute to academic and project-related activities. 4. Perform and support other research, academic, and project-related activities/work assigned by the supervisor from time to time.
Essential Qualification	Master's degree (M.E./M.Tech./M.Sc./MCA or equivalent) in Electronics/ECE/Electrical Engineering/Computer Science/IT/Physics/Materials Science or a relevant allied discipline with a minimum of 55% marks or equivalent CGPA, and meeting the Ph.D. admission eligibility criteria of the University.
Desirable Qualification	NET/GATE/JRF or equivalent qualification; prior research/project experience in Electronics & IT or allied emerging areas; and research publications, patents, programming, simulation, or experimental skills will be preferred.
Date of Interview	Shortlisted candidates will be notified via email for an interview. Interviews will be conducted in-person at Sharda University, Greater Noida, India.
Last Date of Applications	25/09/2026
Terms & Conditions	As per the guidelines of PhD (full-time) candidate under 'Visvesvaraya PhD Scheme for Electronics & IT (Phase-II)', funded by Ministry of Electronics and Information Technology, Govt. of India and Sharda University, Greater Noida, 201310, India

- Interested candidates are requested to submit a detailed softcopy of their CV with all relevant Essential Qualification Degree Certificates, Marks Sheets from 10th onwards, GATE/NET qualifying certificates (if any), Date of Birth Proof Certificate in a **single PDF** file to pallavi.gupta2@sharda.ac.in on or before 25/09/2026.
- Only shortlisted candidates will be called for an interview. Selected candidates will be informed by email. Travel, accommodation and food expenses will not be covered. The institute & funding agency reserves all rights regarding the selection process. The appointment for PhD is purely temporary and academic. It does not confer any right to adopt regular appointment, absorption, or continuation in the University or the funding agency beyond the project tenure.

Dean
Sharda School of Engineering & Science
Sharda University, Greater Noida, India

Digital India Corporation
(A Section 8 Company)
Ministry of Electronics and Information Technology,
Govt. of India

डिजिटल इंडिया कॉर्पोरेशन
(धारा 8 कंपनी)
इलेक्ट्रॉनिक्स और सूचना प्रौद्योगिकी मंत्रालय,
भारत सरकार

PhD Cell

Ref.: Implementation Order No: PhD-06/09-2026/120 **Date: 31 Aug, 2026**

Subject: **Implementation Order for the execution of 'Visvesvaraya PhD Scheme for Electronics and IT: Phase II' at Sharda University, Greater Noida, Uttar Pradesh.**

We are pleased to convey the approval of the allotment of 1 Full-Time PhD seat for implementation of 'Visvesvaraya PhD Scheme for Electronics and IT: Phase II' at Sharda University, Greater Noida, Uttar Pradesh for **Academic Year 2026-27**.

2. The total estimated budget outlay for the allocated candidate is Rs. 44,45,000/- (Rupees Forty-Four Lakh Forty Five Thousand only) for a duration of 60 months or PhD completion, whichever is earlier. Detailed guidelines are provided in the attached Annexure.

3. Prior to candidate enrollment, we request the Head of the Institution to accept the 'Terms and Conditions', 'Standard Operating Procedure', and execute the Surety Bond (duly signed and stamped). Kindly upload soft copies to the PhD portal and email them to phd-scheme@digitalindia.gov.in by the end of September 2026. The original signed documents should be dispatched to:

Sandeep Bansal, In-charge, PhD Cell, Room No. 2084, Electronics Niketan, 6, CGO Complex, Lodhi Road, New Delhi-110003.

4. The enrolment of PhD candidates followed by their verification & other formalities as per the 'T&C' should be completed by Oct 2026 of the Academic Year 2026-27.

5. The candidates enrolled & verified by the institution would be eligible to receive monthly 'Fellowship & Reimbursement of Rent' through DBT. The institution is required to submit their periodic performance/progress reports. In case of non-performance of any candidate, change in fellowship due to absence/drop-outs etc., or in RoR due to change in accommodation, the institution is required to, intimate PhD Cell, DIC; immediately and obtain the acknowledgment of intimation receipt from PhD Cell. Without such intimation PhD Cell, DIC; will continue to transfer the fellowship & RoR through DBT and recovery of the excess amount would be the responsibility of the institution. For more details, please refer to the Guidelines & SoP as attached.

6. The PhD Cell team will coordinate routine matters with your designated Nodal Officer and will reach out to the Head of the Institution if direct intervention is required.

Visvesvaraya PhD Scheme · Digital India Corporation · In-Charge PhD Cell · Ministry of Electronics & IT

Sandeep Bansal
(In-charge, PhD Cell, DIC)

Copy to:
1. Prof. Vrince Vimal, Nodal Officer, Sharda University, Greater Noida, Uttar Pradesh
2. Shri Akhil Kumar, IPS, MD&CEO, Digital India Corporation
3. Shri K. P. Sivadas, Director (Finance), Digital India Corporation